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<u>The operational semantics of a Java Secure Processor - Michael (1998)</u> (Correct) (25 citations)
software that has to be run on a smart card **processor. Java** [4] was originally designed for writing
www.dsse.ecs.soton.ac.uk/techreports/1998/dsse-tr-98-1.ps.gz

<u>Symbolic Simulation of the JEM1 Microprocessor - Greve (1998)</u> (Correct) (15 citations)
in development of the JEM1, the world's first **Java processor**. We begin in Section 3 with a brief overview
The JEM1 is the world's first direct execution **Java processor** [13, 4]a processor whose instruction set is
www.comic.net/~dagreve/publications/symbolic.ps.gz

<u>Architectural Issues in Java Runtime Systems - Radhakrishnan, Vijaykrishnan.. (1999)</u> (Correct) (15 citations)
to develop hardware runtime support, such as **Java processors** [8, 9]to execute the bytecodes. There are
Specialized hardware support, such as **Java processors**, though efficient, may not be sufficiently
www.ece.utexas.edu/projects/ece/lca/ps/hpca2k.ps

<u>Harissa: A Hybrid Approach to Java Execution - Muller, Schultz (1999)</u> (Correct) (11 citations)
hardware support and dynamic compilation. A **Java processor** is a dedicated processor that implements the
www.daimi.aau.dk/~ups/papers/ieee99-tmp.ps.gz

<u>A JAVA ILP Machine Based on Fast Dynamic Compilation - Ebcioglu, Altman, Hokenek (1997)</u> (Correct)
(11 citations)
fkemal,erik,hokenekg@watson.ibm.com Keywords: **JAVAProcessors**, Instruction-level parallelism, Object code
computing and JAVA gain importance, dedicated **JAVA processors** designed for running JAVA programs
cardit.et.tudelft.nl/~steven/ilp/ebcioglu97-java.pdf

<u>Power-Sensitive Multithreaded Architecture - Seng, Tullsen, Cai (2000)</u> (Correct) (10 citations)
the Compaq Alpha EV8 [5] and the Sun MAJC **Java processor** [12]joining the existing Tera MTA
www-cse.ucsd.edu/users/tullsen/iccd00.pdf

<u>Jaguar: Enabling Efficient Communication and I/O in Java - Welsh, Culler</u> (Correct) (9 citations)
of previous work has addressed problems with **Java processor** performance, namely, ef- ciency of compiled
now.cs.berkeley.edu/~mdw/papers/jaguar-journal.ps.gz

<u>Object-Oriented Architectural Support for a Java.. - Vijaykrishnan.. (1998)</u> (Correct) (8 citations)
Object-Oriented Architectural Support for a **Java Processor** N. Vijaykrishnan 1 N. Ranganathan 1
Object-Oriented Architectural Support for a **Java Processor** 331 2. A Just-in-time (JIT) compiler is an
www.ifs.uni-linz.ac.at/~ecoop/cd/papers/1445/14450330.pdf

<u>The design of the Inferno virtual machine - Winterbottom, Pike (1997)</u> (Correct) (8 citations)
By contrast, whatever cost gains an integrated **Java processor** might realize will likely be lost in the
rum.cs.yale.edu/jvmsem/doc/inferno.ps

<u>Techniques for Obtaining High Performance in Java Programs - Kazi, Stanley, Lilja.. (1999)</u> (Correct)
(6 citations)
alternative for executing Java programs is a **Java processor** that implements the JVM directly in hardware.
the instruction set for a (possibly virtual) **Java processor**. Javawas designed to implement highly
instruction set for a (possibly virtual) **Java processor**. Javawas designed to implement highly reliable or
www.msi.umn.edu/general/Reports/rptfiles/UMSI99-1/UMSI99-1.ps.Z

<u>Java Runtime Systems: Characterization and.. - Radhakrishnan.. (2001)</u> (Correct) (5 citations)
benefit from this type of execution. 4. A **Java processor** is an execution model that implements the JVM
object addressing and symbolic resolution. **Java processors** can be cost-effective to design and deploy
www.ece.utexas.edu/projects/ece/lca/ps/ramesh-java-toc.pdf

Allowing for ILP in an Embedded Java Processor - Radhakrishnan, Talla, John (2000) (Correct) (5 citations)
Allowing for ILP in an Embedded **Java Processor** Ramesh Radhakrishnan, Deependra Talla and
Abstract **Java processors** are ideal for embedded and network computing
www.ece.utexas.edu/projects/ece/lca/ps/isca2.ps

Performance Evaluations and Chip-Space.. - Kreuzinger.. (Correct) (3 citations)
This paper introduces a multithreaded **Java processor** kernel which is specifically designed as
enhanced by an adapted JVM. **Java processors** execute Java bytecode instructions directly
www-ima.ira.uka.de/mitarbeiter/brinks/ps/iccd00.ps

Java Signal Processing: FFTs with bytecodes - John Glossner Jesse (1998) (Correct) (3 citations)
compiler technology and direct execution on **Java processors** such as the Delft-Java multithreaded
and Java. Wehave also designed the Delft-**Java processor**#4#An important feature of this
www.cs.ucsb.edu/conferences/java98/papers/jfft.pdf

Execution Characteristics of Just-In-Time Compilers - Radhakrishnan, Rubio.. (1999) (Correct) (3 citations)
to an intermediate language like C [17]and **Java processors** [11]1.1 Motivation There has been a lot
the behavior of Java bytecode execution for a **Java processor** and proposed architectural features based on
www.ece.utexas.edu/projects/ece/lca/ps/tr990717.ps

Hybrid Jini for Limited Devices - Lenders, Huang, Muheim (2001) (Correct) (2 citations)
second solution is to use embedded boards with **Java processors**. Unlike traditional microprocessors, which
[provides a development board with their **Java processor** called aJile. Another solution is provided by
www.tik.ee.ethz.ch/~huang/publication/hybrid-jini-tr.ps.gz

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